

10/560758

CFC

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December 19, 2007

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Re: **CERTIFICATE OF CORRECTION**
U. S. Letters Patent No. : 7,242,038 B2
Issued: Jul. 10, 2007
For: Heterostructure Bipolar Transistor
Inventor: Oda et al.
Our File No. 096790.P515

Certificate
JAN 16 2008
of Correction

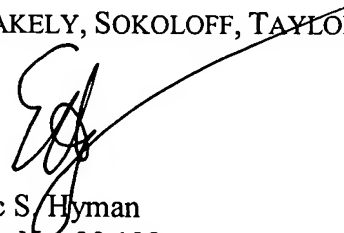
Dear Sir:

Enclosed is a Certificate of Correction (two copies) for the above-referenced patent.

This request for correction is made under rule 322 of the Rules of Practice and 35 U.S.C. Section 254.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP


Eric S. Hyman
Reg. No. 30,139RECEIVED-USPTO
Patent Publication

JAN 16 2008

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PTO Form- 1449RECEIVED-USPTO
Patent Publication

JAN 16 2008

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO: 7,242,038

DATED: Jul. 10, 2007

INVENTOR(S): Oda et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Item [54] please delete "HETEROJUNCTION" and insert - - HETEROSTRUCTURE - -.

MAILING ADDRESS OF SENDER:
BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
12400 Wilshire Blvd. 7th floor
Los Angeles, CA 90025-1030

PATENT NO. 7,242,038

Certificate of Correction (PTO Form 1050)-Amended

1 of 1

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Patent Registration
JAN 16 2008

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